



JAN 26 2004

TRANSMITTAL OF FORMAL DRAWINGS

Docket No.
BUR920000201US2

Re Application Of: Jerry D. Hayes

Serial No.	Filing Date	Confirmation No.	Examiner	Art Unit
09/683,546	01/16/2002		Thompson, Annette M.	2825
Invention: A CIRCUIT AND METHOD FOR MODELING I/O				

Address to:
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Transmitted herewith are:

13 sheets of formal drawing(s) for this application.

Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).

Signature

Richard M. Kotulak, Reg. No. 27,712
IP Law Department, 972E
IBM Corporation
1000 River Street
Essex Junction, VT 05452
Telephone: 802-769-4457

Dated:

I certify that this document and attached formal drawings are being deposited on 01/22/2004 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature of Person Mailing Correspondence

Typed or Printed Name of Person Mailing Correspondence